

EMBEDDED DESIGNS & SOLUTIONS SEMINAR



Schedule:

Seminar registration for delegates	0900 - 0915
OPENING SPEECH & INTRODUCTION TO XILINX Philip Wong Director Xilinx APAC-Sinagpore	0915 - 0945
Track 1: EMBEDDED DESIGN - HOW TO GET IT DONE Glenn West Regional Network Manager AAR Corp-Singapore	0945 - 1030
Tea Break	1030 - 1045
Track 2: RUNNING RTOS ON XILINX EMBEDDED PROCESSOR Lim, Hong Swee Sr. Manager DSP & Embedded Product Marketing Xilinx APAC-Singapore	1045 - 1115
Track 3: DSP DESIGN AUTOMATION USING XILINX FPGAS Lim, Hong Swee Sr. Manager DSP & Embedded Product Marketing Xilinx Asia Pacific-Singapore	1115- 1215
Lunch	1215- 1300
Track 4: POWER SOLUTIONS FOR FPGA Kuldeep Dhillon Director-Global Business Development Murata Power - USA	1300 - 1400
Tea Break	1400 - 1415
Track 5: WHERE EMBEDDED LINUX IS TODAY Julian Hsu Director-Business Development MontaVista-Taiwan	1415 - 1515
Track 6: ULTRA LOW POWER WIRELESS SENSOR NETWORKS Salas Lau Engineer, Field Applications Nordic Wireless- Hong Kong	1515 - 1615
Lucky Draw & Closing Note	1615 - 1630